Oblivious Data Structures

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Oblivious RAM is a cryptographic primitive for provably obfuscating access patterns to data.

Hierarchical ORAMs [Goldreich'87] [GO'96] [KLO'12] Bandwidth Overhead: = $\frac{\text{Data transferred in oblivious case}}{\text{Data transferred in non-oblivious case}}$

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Hierarchical ORAMs [Goldreich'87] [GO'96] [KLO'12]

Tree-based ORAM [SCSL'11] [SDSCFRYD'13]

Can we do better?

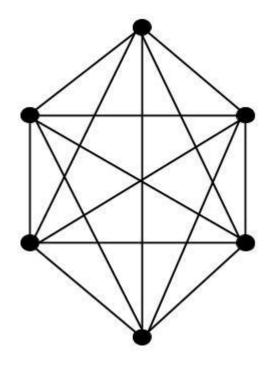
Can we do better?

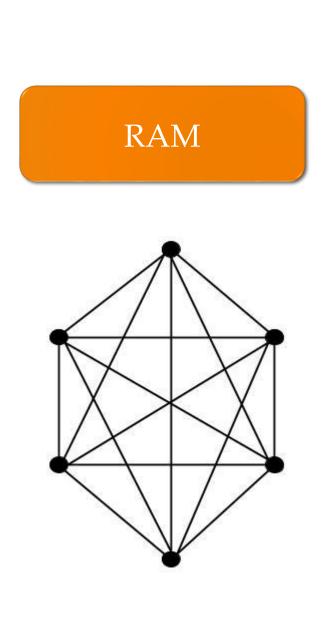
Path ORAM partially solves this problem

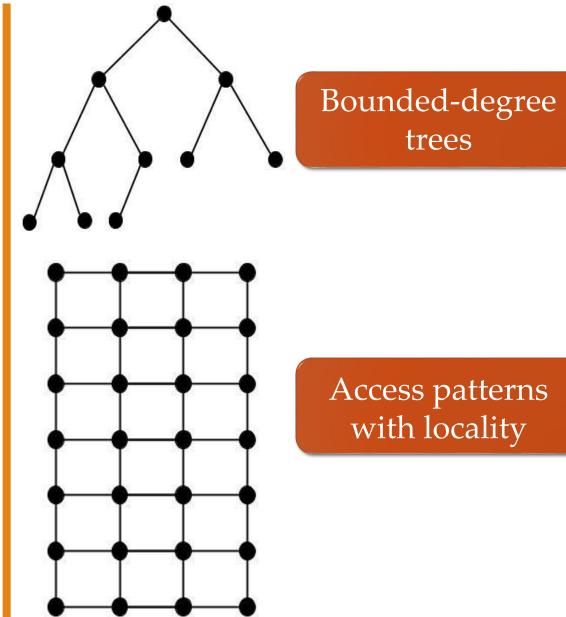
Can we do better?

Can we do better for restricted access patterns?







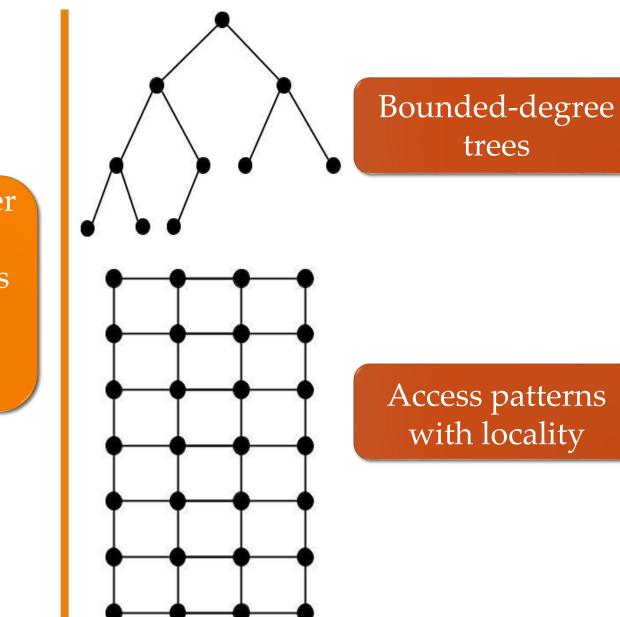


Can we do better for these restricted access patterns?

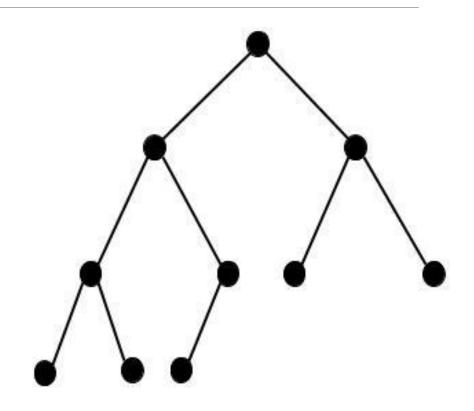
Bounded-degree trees Access patterns with locality

Can we do better for these restricted access patterns?

YES



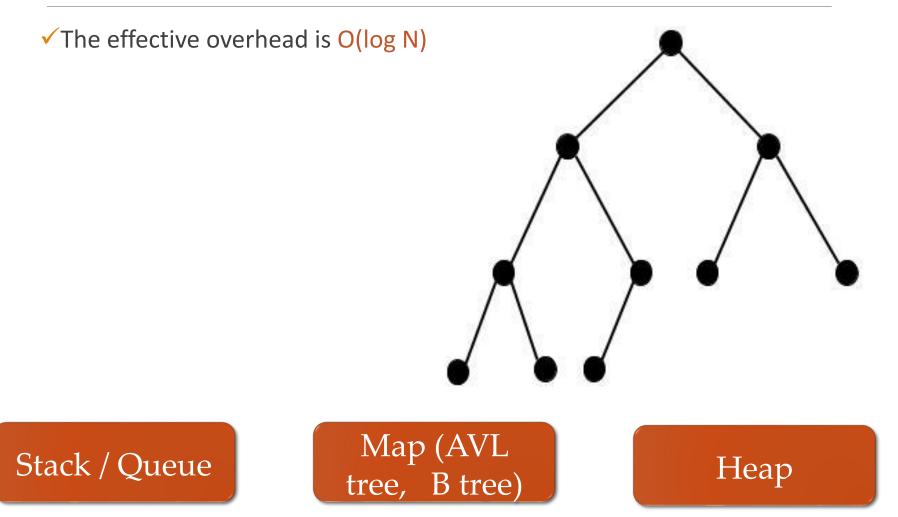


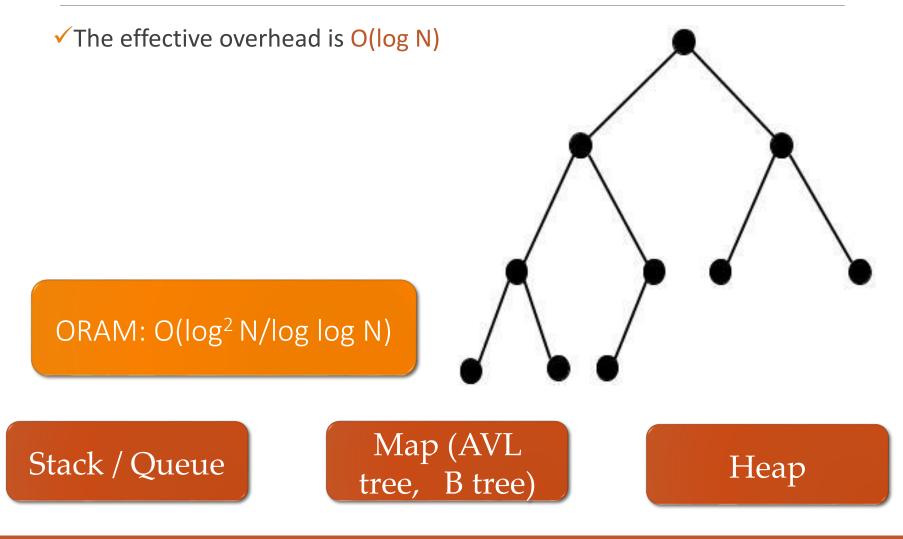




Map (AVL tree, B tree)







✓ The effective overhead is O(log N)

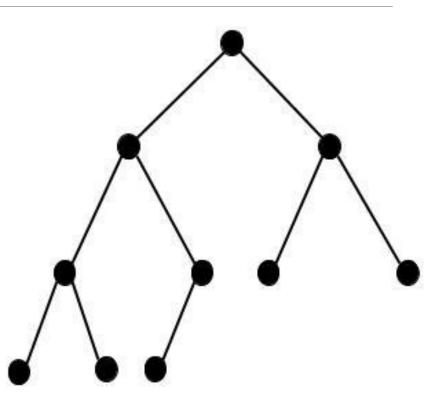
✓ Inspired by [GGHJRW'13]

✓Speedup

✓ Bandwidth overhead 12x – 16x

 \checkmark Circuit size 10x - 14x

Compared with Path ORAM; data size 2³⁰

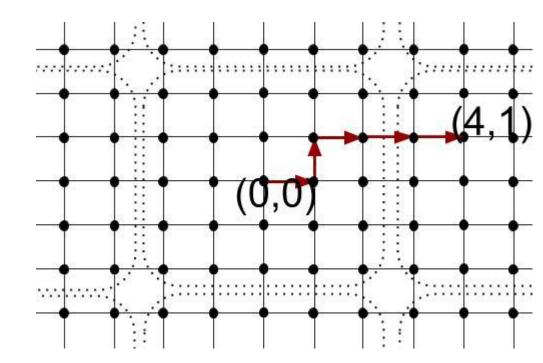


Heap

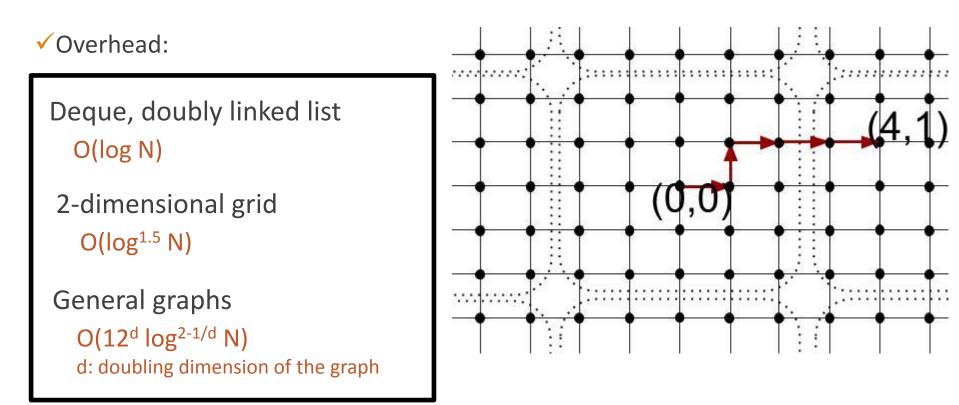


Map (AVL tree, B tree)

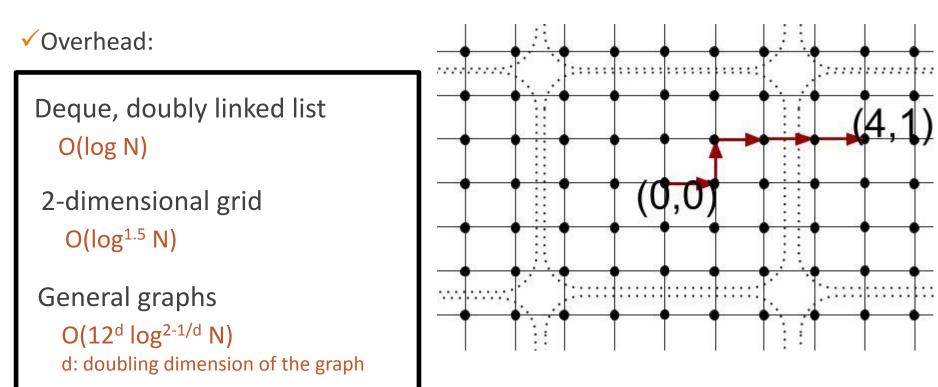
Access patterns with locality



Access patterns with locality



Access patterns with locality



 Bandwidth overhead speedup for deque, doubly linked list - 9x

Compared with Path ORAM; data size 2³⁰



Map (AVL tree)

Heap

Deque

Doubly linked list Open source implementation on a garbled circuit backend coming soon

Oblivious Data Structures: [WNLCSSH'14]

Thank You! kartik@cs.umd.edu